

The UTLEON3 Softcore: Structure and Evaluation

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Outline

- 1 Microthreading and UTLEON3
- 2 UTLEON3 efficiency
 - Pipeline efficiency
 - HWFAM
 - Implementation results
- 3 Long-latency operations
 - Pipelined long-latency operations
 - Non-pipelined long-latency operations
- 4 Hardware families of threads
 - Multi-core configurations, homogeneous/heterogeneous
 - Microthreaded coupling of the GP and SP cores
 - Evaluation
- 5 Conclusions

